

ABSTRACT OF THE DISCLOSURE

When a plurality of processors are connected in a loop by a one-way bus, a command packet addressed to a main processor is transmitted to the one-way bus so as to easily specify a fault portion. In accordance with whether or not the command packet is returned to the main processor for a predetermined time, it is determined whether or not the fault exists at any portion in an electronic device. When the fault exists, a test signal is transmitted to sub-processors via a dedicated signal line. The fault portion in the electronic device is specified based on whether or not, for a predetermined time, a test packet transmitted via the one-way bus in accordance with the test signal is normally received from the sub-processor as the transmission destination of the test signal.